

New Modeling of UPFC for Power Flow Study and Setting Parameters to Increase Voltage Level and Reduce Power Losses

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(Abstract) The Unified Power Flow Controller (UPFC), is one of the most complex Flexible alternating-current transmission systems (FACTS) devices in a power system. The UPFC is the shunt-series device that provides power flow control together with separate voltage control. The objective of this paper is propose a UPFC model that can be incorporated into existing MATLAB based MATPOWER for power flow studies. Power flow is the analysis to determine the steady-state magnitude and angle voltages at all buses of the power system and also the real and reactive power flows in every transmission line. The proposed model in order to increase voltage level and reduce power losses is tested on IEEE 14-bus system.

Keywords: UPFC; Injection model; Power Flow; MATPOWER; MATLAB.

1. INTRODUCTION

Nowadays, several important issues related to power system have been discussed worldwide. Some of the serious issues are the power quality, transmission loadability, congestion management, reduce power losses and voltage stability. To overcome these issues, best approach is using FACTS devices. In the late 1980s the Electric Power Research Institute (EPRI) has presented a new technology known as FACTS [1].

One of the most general FACTS devices is UPFC, which has been introduced by Gyugyi in 1991[2]. The structure of UPFC that shown in Figure 1, consisting of two "back to back" AC to DC voltage source converters (VSC) operated from a common DC link capacitor. First converter (converter 1 or shunt converter) is connected in shunt and the second converter (converter 2 or series converter) in series with the transmission line. The shunt converter is mainly used to supply active power demand of the series converter via a common DC link. Converter 1 can also generate or absorb reactive power, if it is desired, and thereby provide independent shunt reactive compensation for the line. Converter 2 provides the basic function of the UPFC by injecting a voltage with controllable amplitude and phase angle in series with the line via a voltage source [3], [4].

Several papers can be found on development of UPFC steady state and dynamic models [5] - [8]. Steady state model referred as an injection model is described in [5]. This model is usually used for UPFC modeling.

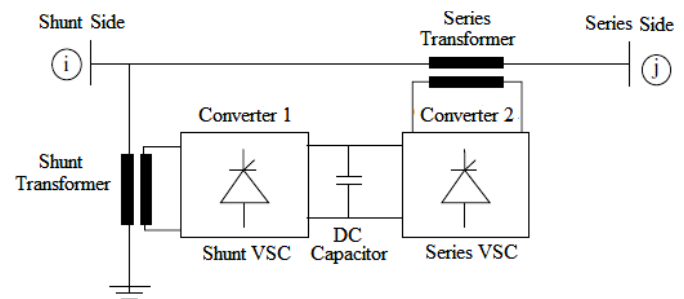


Figure 1. Structure of UPFC.

The rest of the paper is organized as follows: injection model of UPFC is described in section 2. The proposed method for UPFC modeling is presented in Section 3. Simulation results along with some observations are discussed in Section 4. In this section IEEE 14-bus test system is used for the case studies. The paper ends with a summary conclusion in the final section

2. UPFC INJECTION MODEL

To obtain UPFC injection model, it is first essential to consider the series voltage source, Figure 2.

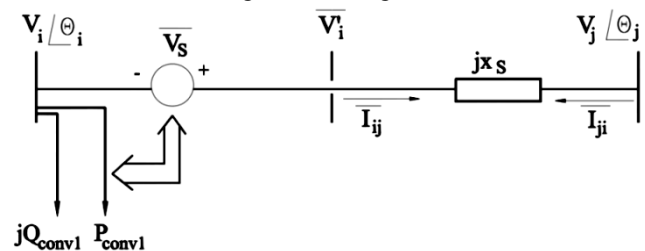


Figure 2. The UPFC electric circuit [3].

The reactance x_s describes a reactance seen from terminals of the series transformer and is equal to (in p.u. base on system voltage and base power) [3], [4]:

$$x_s = x_k r_{max}^2 \left(\frac{S_B}{S_S} \right) \quad (1)$$

$$b_s = -\frac{1}{x_s} \quad (2)$$

That

x_k : The series transformer reactance.

r_{max} : The maximum value of injected voltage amplitude (p.u.).

S_B : The system base power.

$S_S = S_{conv2}$: The nominal rating power of the series converter.

Voltage source connected in series is modeled with an ideal series voltage (\bar{V}_s) the amplitude and phase is controlled [3], [4].

$$\begin{aligned} \bar{V}_s &= r \bar{V}_i e^{j\gamma} \\ 0 &\leq r \leq r_{max}^2 \\ 0 &\leq \gamma \leq 2\pi \end{aligned} \quad (3)$$

That

r : The value of injected voltage amplitude (p.u.).

γ : The value of injected voltage angle.

The equations of the UPFC injection model (Figure 3) are given as [3], [4]:

$$P_{si} = -rb_s V_i V_j \sin(\theta_i - \theta_j + \gamma) \quad (4)$$

$$Q_{si} = -rb_s V_i^2 \cos(\gamma) + Q_{conv1} \quad (5)$$

$$P_{sj} = rb_s V_i V_j \sin(\theta_i - \theta_j + \gamma) \quad (6)$$

$$Q_{sj} = rb_s V_i V_j \cos(\theta_i - \theta_j + \gamma) \quad (7)$$

$$\begin{aligned} P_{i1} &= -rb_s V_i V_j \sin(\theta_i - \theta_j + \gamma) \\ &\quad - b_s V_i V_j \sin(\theta_i - \theta_j) \end{aligned} \quad (8)$$

$$\begin{aligned} Q_{i1} &= -rb_s V_i^2 \cos(\gamma) + Q_{conv1} - b_s V_i^2 \\ &\quad + b_s V_i V_j \cos(\theta_i - \theta_j) \end{aligned} \quad (9)$$

$$\begin{aligned} P_{j2} &= rb_s V_i V_j \sin(\theta_i - \theta_j + \gamma) \\ &\quad + b_s V_i V_j \sin(\theta_i - \theta_j) \end{aligned} \quad (10)$$

$$\begin{aligned} Q_{j2} &= rb_s V_i V_j \cos(\theta_i - \theta_j + \gamma) - b_s V_j^2 \\ &\quad + b_s V_i V_j \cos(\theta_i - \theta_j) \end{aligned} \quad (11)$$

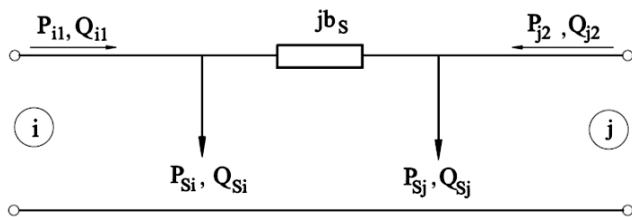


Figure 3. Injection model of the UPFC [3].

3. UPFC MODEL AND ALGORITHM

In this section we propose a new method for modeling UPFC based on injection model. The model is simple and beneficial in understanding the UPFC impact on the power system. In addition, the proposed model for modeling UPFC in the MATLAB is very efficient. Figure 4, shows the proposed model for the UPFC.

In this case UPFC can be modeled by a load at the sending bus (PQ bus, satisfy the equations 4 and 5) and an “equivalent PQ generator” at the receiving bus (PQ bus, satisfy the equations 6 and 7). Using this model, two virtual buses are added to the network.

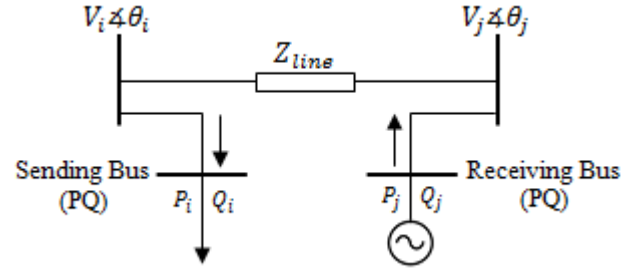


Figure 4. New load flow model.

That

$$Z_{line} = R_{line} + j(X_{line} + X_s) \quad (12)$$

Power required at the sending bus is set to the desired real and reactive powers at that bus. Reactive power is injected (in sending bus) order to achieve the specified voltage magnitude. The flowchart of power flow algorithm with/without presence of UPFC shown in Figure 5.

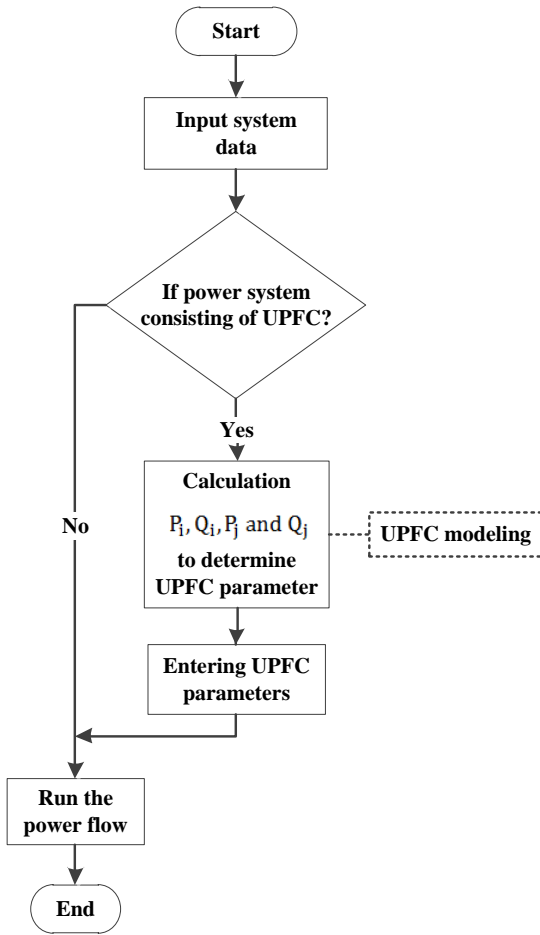


Figure 5. Power flow algorithm.

4. SIMULATION RESULTS

In this section, the UPFC parameters are set to increase voltage level and reduce power losses. The IEEE 14-bus test system is shown in Fig. 6 to demonstrate the effectiveness and validity of the proposed model. The system data is found in [9]. In this network active and reactive power of buses (load) 9, 10 and 14 are changed. The changes of power are shown in Table 1. MATPOWER, a toolbox of MATLAB, has been used for simulations and UPFC modeling [10].

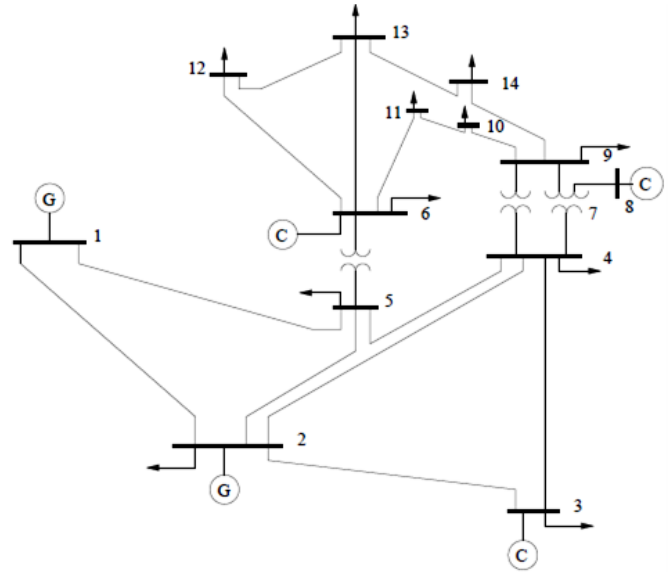


Figure 6. IEEE 14-bus test system.

Table 1. Power changes.

Bus number (Load)	Active power (MW)	Reactive power (MVar)
9	29.5→39.5	16.6→30
10	9→36	5.8→15.8
14	14.9→54.9	5→30

Table 2 shows the power flow result without presence of UPFC.

Table 2. Power Flow Result without Presence of UPFC.

Bus number	Voltage amplitude (pu)	Power losses	
		Active (MW)	Reactive (MVar)
1	1.06	8.39	25.62
2	1.045		
3	1.01		
4	0.992		
5	0.998		
6	1.07		
7	1.022		
8	1.09		
9	0.988		
10	0.98		
11	1.02		
12	1.041		
13	1.019		
14	0.904		

According to the Table 2, bus 14 as sending bus and bus 13 as receiving bus (line 13-14) are chosen for installing UPFC in order to increase voltage level of bus 14 and decrease power losses.

The UPFC parameters are given in Table 3.

TABLE I. Table 3. UPFC Parameters.

UPFC parameters	Value
x_k	0.01 pu
r_{max}	0.08 pu
$S_S = S_{conv 2}$	50 MVA

The voltage magnitude of bus 14 have been shows in Figure 7 for the case when varying both the injected voltage magnitude (r) from 0.01 to 0.08 pu and the injected voltage angle (γ) for 0, 120 and 240 degree.

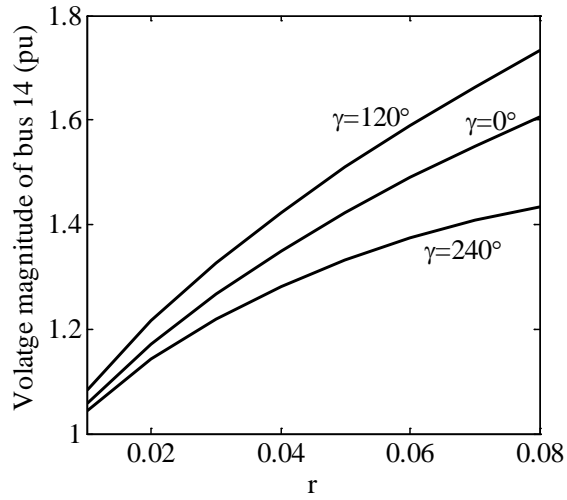


Figure 7. Voltage magnitude of bus 14 (pu).

The active power losses have been shows in Figure 8 for the case when varying both the injected voltage magnitude (r) from 0.01 to 0.08 pu and the injected voltage angle (γ) for 0, 120 and 240 degree.

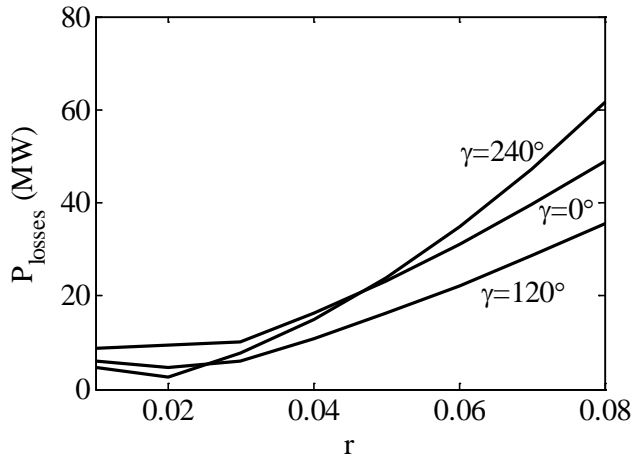


Figure 8. Active power losses (MW).

Also the voltage magnitude of bus 14 have been shows in Figure 9 for the case when varying both injected reactive power (Q_{conv1}) from -100 to 100 MVar and the injected voltage magnitude (r) from 0 to 0.08 (assuming $\gamma = 120^\circ$).

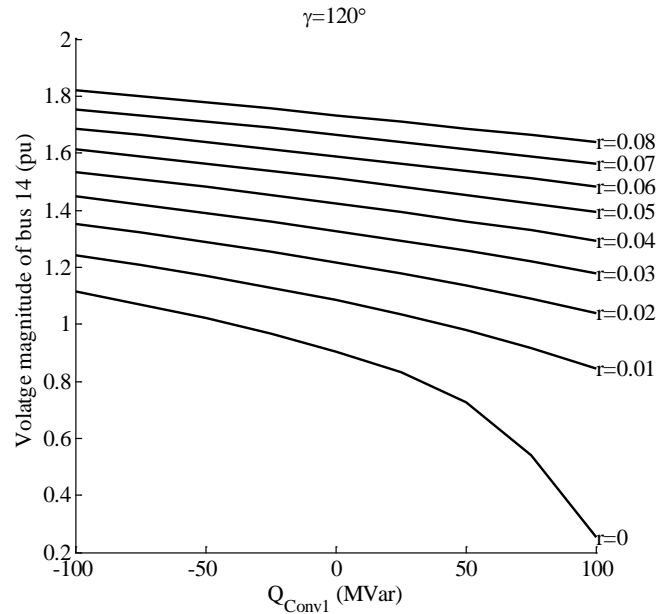


Figure 9. Voltage magnitude of bus 14 (pu), $\gamma = 120^\circ$.

Power flow results for UPFC Installed at bus 14 (line 13-14) for various r and γ are given in Table 4 to Table 7 (assuming $Q_{conv1} = 0$).

Table 4. Power Flow Result with Presence of UPFC.

$r = 0.01 \text{ pu and } \gamma = 240^\circ$			
Bus number	Voltage amplitude (pu)	Power losses	
		Active (MW)	Reactive (MVar)
1	1.06	4.86	14.83
2	1.045		
3	1.01		
4	1.011		
5	1.015		
6	1.07		
7	1.046		
8	1.09		
9	1.028		
10	1.013		
11	1.037		
12	1.055		
13	1.052		
14	1.04		

Table 5. Power Flow Result with Presence of UPFC.

$r = 0.01 \text{ pu and } \gamma = 120^\circ$			
Bus number	Voltage amplitude (pu)	Power losses	
		Active (MW)	Reactive (MVar)
1	1.06	6.16	18.82
2	1.045		
3	1.01		
4	1.009		
5	1.012		
6	1.07		
7	1.053		
8	1.09		
9	1.044		
10	1.026		
11	1.044		
12	1.061		
13	1.06		
14	1.079		

Table 6. Power Flow Result with Presence of UPFC.

$r = 0.015 \text{ pu and } \gamma = 120^\circ$			
Bus number	Voltage amplitude (pu)	Power losses	
		Active (MW)	Reactive (MVar)
1	1.06	5.35	16.33
2	1.045		
3	1.01		
4	1.015		
5	1.017		
6	1.07		
7	1.065		
8	1.09		
9	1.065		
10	1.044		
11	1.053		
12	1.069		
13	1.076		
14	1.148		

Table 7. Power Flow Result with Presence of UPFC.

$r = 0.01 \text{ pu and } \gamma = 0^\circ$			
Bus number	Voltage amplitude (pu)	Power losses	
		Active (MW)	Reactive (MVar)
1	1.06	8.6	26.25
2	1.045		
3	1.01		
4	0.999		
5	1.002		
6	1.070		
7	1.046		
8	1.09		
9	1.037		
10	1.021		
11	1.041		
12	1.058		

13	1.051		
14	1.053		

According to Table 4 to 7, if $r = 0.01 \text{ pu}$ and $\gamma = 240^\circ$ the best answer can be obtained so that, power losses than base case (without UPFC) is decreased and voltage level of buses 4, 5, 9, 10 and 14 are increased.

5. CONCLUSION

This paper deals with the FACTS device known as the Unified Power Flow Controller that is used to maintain and improve power system operation and stability. The UPFC model for power flow studies that easier other models to implement in MATLAB is proposed. MATPOWER is used for UPFC modeling and simulation. The proposed model is tested on IEEE 14-bus system. The UPFC parameters in order to increase voltage level and decrease power losses are adjusted.

REFERENCES

- [1] N. G. Hingorani, "Flexible AC Transmission", *IEEE Spectrum*, vol. 30, no. 4, pp. 40-45, April 1993.
- [2] L. Gyugyi, "A Unified Power Flow Control Concept for Flexible AC Transmission Systems", *IEE Proceedings-C*, vol. 139, no. 4, pp. 323-331, July 1992.
- [3] N. Dizdarevic, "Unified Power Flow Controller in alleviation of voltage stability problem", Ph.D. thesis, University of Zagreb, Faculty of Electrical Engineering and Computing, Dept. Power Systems, October 2001.
- [4] S. Izadpanah Tous, and M. Gorji, "Unified Power Flow Controller and its working modes", Presented at 2011 World Congress on Engineering and Technology, China, 2011.
- [5] M. Noroozian, L. Angquist, M. Ghandhari, and G. Andersson, "Use of UPFC for Optimal Power Flow Control," *IEEE Transactions on Power Delivery*, vol. 12, no. 4, pp. 1629-1634, October 1997.
- [6] A. Nabavi-Niaki, and M. R. Iravani, "Steady-state and Dynamic Models of Unified Power Flow Controller (UPFC) for Power System Studies", *IEEE Transactions of Power Systems*, vol. 11, no. 4, pp. 1937-1943, November 1996.
- [7] E. M. Saied, and M. A. EL-Shibini, "Fast reliable unified power flow controller (UPFC) algorithm Steady-state and Dynamic Models of Unified Power Flow Controller (UPFC) for Power System Studies", *Seventh International Conference on AC-DC Power Transmission*, 2001, pp. 321-326.
- [8] A. J. F. keri, A. S. Mehraban, X. Lombard, A. Eiriachy, and A. A. Edris, "Unified power flow controller (UPFC): modeling and analysis", *IEEE Transactions of Power Delivery*, vol. 14, no. 2, pp. 648-654, 1999.
- [9] Power Systems Test Case Archive: 'IEEE 14 Bus Power Flow Test Case', University of Washington, 2003. Available at: www.ee.washington.edu/research/pstca/pf14/pg_tca14bus.htm.
- [10] MATPOWER, a MATLAB Power System Simulation Package, Version 4. 1, Available at: <http://www.pserc.cornell.edu/matpower>.

Author Introduction



Sajjad Ahmadnia was born in Mashhad, Iran, in 1990. He received the B.S. degree in electrical engineering from the Sadjad Institute for Higher Education of Mashhad, Iran in 2012. Currently, His research interest is in FACTS devices and control.



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